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11044 U.S. PTO 10/26/01

UTILITY PATENT APPLICATION TRANSMITTAL

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Title of Invention	**Lateral Transistor Having Graded Base Region, Semiconductor Integrated Circuit And Fabrication Method Thereof**
Named Inventor(s)	**Makoto Yamamoto; Akio Iwabuchi**
Attorney Docket	**44471-265522 (13700)**
Express Mail Label No.	EL910717824US

JC760 U.S. PTO 10/014949 10/26/01

APPLICATION ELEMENTS

1. ☒ Fee Transmittal Form *(Submit an original, and a duplicate for fee processing)*
2. ☐ Applicant claims Small Entity status
3. ☒ Specification, Claims, and Abstract — Total Pages 23
4. ☒ Drawings — Total Sheets 9, Total Pages 32
5. Oath or Declaration
   a. ☒ Newly executed (original or copy)
   b. ☐ Copy from prior application (37 CFR 1.63(d)) *(for continuation/divisional with Box 16 completed)*
   The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 5b, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.
   (i) ☐ DELETION OF INVENTOR(S) Signed statement attached deleting inventor(s) named in the prior application, see 37 CFR 1.63(d)(2) and 1.33(b).
6. ☐ Microfiche Computer Program *(Appendix)*

ADDRESS TO: Assistant Commissioner for Patents
Box Patent Application
Washington, D.C. 20231

ACCOMPANYING APPLICATION PARTS

7. ☐ Nucleotide and/or Amino Acid Sequence Submission *(if applicable, all necessary)*
   a. ☐ Computer Readable Copy
   b. ☐ Paper Copy (identical to computer copy)
   c. ☐ Statement verifying identity of above copies
8. ☒ Assignment:
   a. ☒ Assignment Papers (cover sheet & document(s))
   b. ☐ Assignment is of record in parent application No. _____
9. ☐ 37 CFR 3.73(b) Statement *(when there is an assignee)*
   ☐ Power of Attorney by assignee
10. ☐ English Translation Document *(if applicable)*
11. ☐ Information Disclosure Statement (IDS) PTO-1449
    ☐ Copies of IDS Citations
12. ☐ Preliminary Amendment
13. ☒ Return Receipt Postcard (MPEP 503) *(Should be specifically itemized)*
14. ☒ Certified Copy of Priority Document(s) Japanese Patent Application No. P2001-128187
15. ☐ Other: _____

16. If a **CONTINUING APPLICATION**, *check appropriate box and supply the requisite information*:
☐ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No:
Recite complete dependency back to first parent application:

17. CORRESPONDENCE ADDRESS

☒ *Customer Number or Bar Code Label*

23370

(Insert Customer No. or Attach bar code label here)

Name (Print/Type)	Roger T. Frost	*Registration No. (Attorney/Agent)*	22,176
Telephone	(404)815-6500		
Signature		*Date* Oct. 26, 2001	October 26, 2001

FEE TRANSMITTAL Attorney Docket No. 4[illegible]71-265522 (13700)

This sheet accompanies a patent application transmittal for the following application:

Inventor(s): **Makoto Yamamoto, et al.**
Filing Date: **October 26, 2001**
Title: **Lateral Transistor Having Graded Base Region, Semiconductor Integrated Circuit And Fabrication Method Thereof**

The filing fee is calculated as shown below:

1. **FILING FEE:**

	SMALL ENTITY		LARGE ENTITY	
FOR:	FEE	FEE PAID	FEE	FEE PAID
☒ UTILITY FILING FEE	$370	$	$740	$740
☐ DESIGN FILING FEE	$165		$330	
☐ PLANT FILING FEE	$255		$510	
☐ REISSUE FILING FEE	$370		$740	
☐ PROVISIONAL FILING FEE	$80		$160	
SUBTOTAL (1)		$		$740

2. **CLAIMS:**

			SMALL ENTITY		LARGE ENTITY	
FOR:	NO. FILED	NO. EXTRA	RATE	FEE	RATE	FEE
TOTAL CLAIMS	19 - 20 =	0	x 9 =	$	x 18 =	$0
INDEP. CLAIMS	3 - 3 =	0	x 42 =		x 84 =	0
☐ MULTIPLE DEPENDENT CLAIM PRESENTED			+140 =		+280 =	
SUBTOTAL (2)				$		$0

3. **ADDITIONAL FEES:**

	SMALL ENTITY		LARGE ENTITY	
FOR:	FEE	FEE PAID	FEE	FEE PAID
☒ LATE FILING, FEE OR OATH	$65	$	$130	$
☐ NON-ENGLISH SPECIFICATION	$130		$130	
☐ OTHER				
SUBTOTAL (3)		$		$

TOTAL FILING FEES: $740

A check is enclosed for the total amount: **$740**

☒ Charge any additional fees required under 37 C.F.R. 1.16 or 1.17 to Deposit Account 11-0855.

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By: _____ Date: October 26, 2001
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